

Insulated Gate Bipolar Transistor Silicon N Channel IGBT

# JB40N065J1P3

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective.

Provides superior performance in demanding switching applications, offering both low on state voltage and minimal switching loss.

#### **Features**

- Enhancement mode type
- Low saturation voltage : VCE (sat) = 1.24 V (Typ) (IC = 40 A)

#### **Typical Applications**

• Current Resonance Inverter Switching Application

### Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Value	Unit
Collector-emitter voltage	$V_{CE}$	650	V
DC collector current, limited by T <sub>jmax</sub>			
$T_C = 25$ °C	$I_{C}$	80	Α
$T_C = 100$ °C		40	
Pulsed collector current, tp limited by T <sub>jmax</sub>	I <sub>Cpuls</sub>	150	Α
Gate-emitter voltage	V	±20	V
Transient Gate-emitter voltage (tp ≤10µs, D < 0.010)	V <sub>GE</sub>	±30	V
Power dissipation $T_C = 25^{\circ}C$	PD	280	W
Power dissipation $T_C = 100^{\circ}C$		140	<b>Y.Y.</b>
Operating junction temperature	Tj	-40 to +175	°C
Storage temperature	T <sub>stg</sub>	-55 to +175	°C

<sup>1.</sup>Using continuously under heavy loads (e.g. the application of hightemperature/current/voltage and the significant change in temperature, etc.) may cause this product todecrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum rating s. 2.The specifications described are tentative and subject to change without notice.

### **Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	Gate		ic
2	С	Collector		
3	Е	Emitter	1 2 3	G E

2023-7-20



### **ELECTRICAL CHARACTERISTICS** ( $T_j = 25^{\circ}C$ unless otherwise specified)

#### **IGBT**

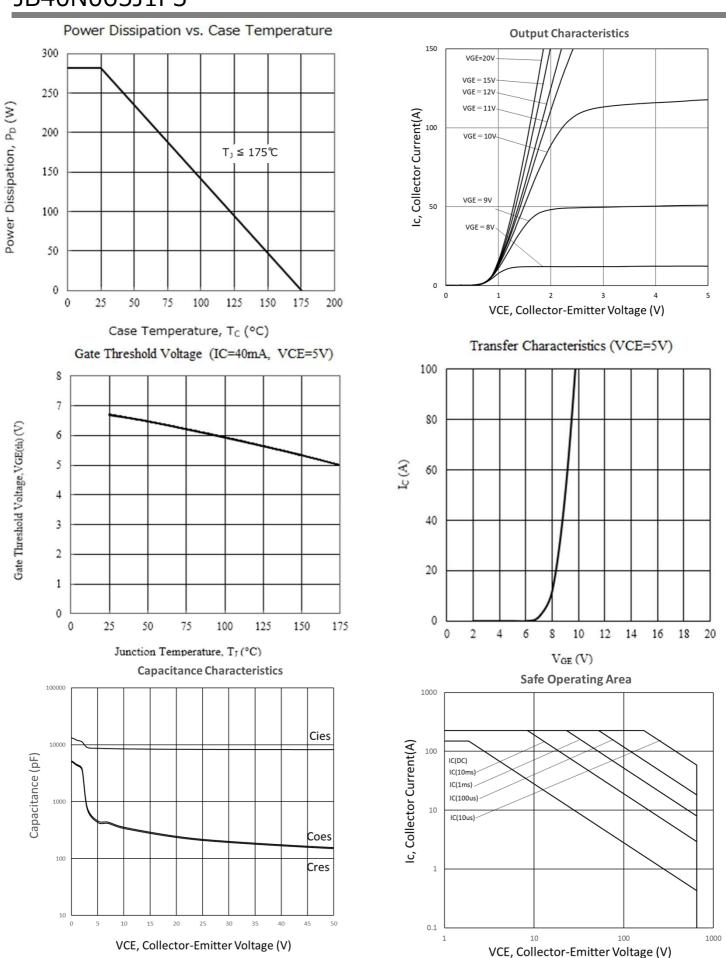
Characteristics		Symbol	Test Condition	Min	Тур	Max	Unit
Gate leakage current		I <sub>GES</sub>	V <sub>GE</sub> =±30 V, V <sub>CE</sub> =0	_	_	±500	nA
Collector cut-off current		I <sub>CES</sub>	V <sub>CE</sub> = 650V, V <sub>GE</sub> =0V	_	_	10	uA
Gate-emitter cut-off voltage		V <sub>GE</sub> (OFF)	$I_{C} = 40 \text{mA}, V_{CE} = 5 \text{V}$	5.7	6.7	7.7	V
Collector-emitter T <sub>j</sub> =25℃			1 400 1/ 151/	_	1.24	1.54	V
saturation voltage	T <sub>j</sub> =100℃	V <sub>CE (sat)</sub>	$I_C = 40A$ , $V_{GE} = 15V$		1.6		v
Gate-emitter threshold voltage		V <sub>GE(th)</sub>	$V_{GE} = V_{CE}, I_{C} = 350 \mu A$	4.8	5.8	6.8	V
Input capacitance		C <sub>ies</sub>		_	8300	_	
Output capacitance		C <sub>oes</sub>	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		215		pF
Reverse transfer capacitance		C <sub>res</sub>			210	_	
Switching time	Rise time	t <sub>r</sub>	T <sub>j</sub> = 25 °C		80	1 <del></del>	
	Turn-on time	t <sub>d(on)</sub>	VCE = 300V, VGE = 0V/15V,	<del>a -</del> 2	120	5 <del></del>	nc
	Fall time	t <sub>f</sub>	RG =10Ω, IC = 40A, L=100 $\mu$ H	-	80		ns
	Turn-off time	t <sub>d(off)</sub>		-	420	_	
Thermal Resistance		R <sub>th</sub> (j-c)		- 1	-	0.54	°C/W

<sup>1.</sup> The specifications described are tentative and subject to change without notice.

<sup>2.</sup>Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated

by the Electrical Characteristics if operated under different conditions.

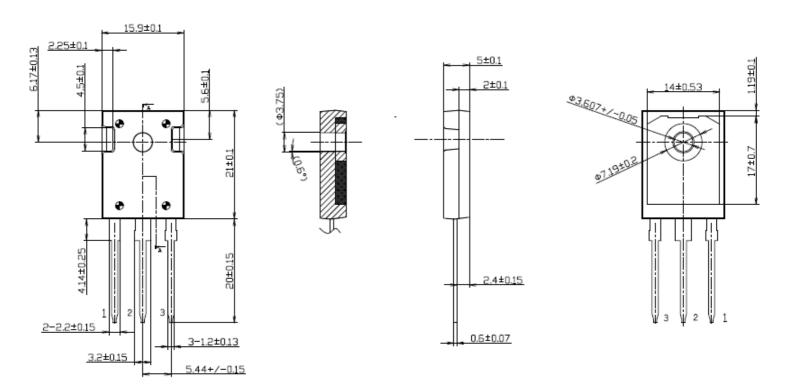




Rev.00000



# **Outline drawing** [Dimensions are in MILLIMETERS]



#### CONNECTION

- **1**:Gate terminal
- 2:Collector terminal
- 3:Emitter terminal

#### Cautions

Please ensure insulation between the heat sink and the product before use. The collector potential on the backside of the products is not intended to be used as a conduction path.



## **Warning**

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